

```

BRS:
IS&R:
BRS: latch



```

⌚ Pending

☒ Active

- ✖ L1: (81495) nand
- ✖ L2: (235345) nonvolatile or (non adj volatile
- ✖ L4: (369907) latch
- ✖ L5: (841193) buffer
- ✖ L6: (289270) string
- ✖ L7: (716) 1 near4 6
- ✖ L8: (21) 7 with (4 or 5)
- ✖ L9: (6039740) common or "same"
- ✖ L10: (1136926) sense
- ✖ L11: (52076) 9 adj2 (4 5 10)
- ✖ L12: (471) (365/185.12).CCLS.
- ✖ L13: (24) 11 and 12
- ✖ L15: (3) ("6058042").PN.
- ✖ L16: (2) ("5966326").PN.
- ✖ L17: (2) ("5748529").PN.
- ✖ L18: (1893614) simultaneous or simultaneously
- ✖ L19: (3118412) substantial or substantially
- ✖ L21: (2) ("6125052").PN.
- ✖ L22: (0) 17 and 21
- ✖ L23: (1) 19 and 21
- ✖ L24: (1425700) program or programming or prog
- ✖ L25: (892008) write or writes or wrote or wri
- ✖ L26: (276956) verified or verify or verifying
- ✖ L27: (1) 24 and 21

Failed

  Saved

 Favorites

 Tagged (0)

 UDC

 Queue

Trash

Search

List

Browse

ជំនួយ

Clear

DBs

US-PGPUB; USPAT; EPO; JPO; DERWENT; IB

Plurals

Default operator: ☒ OR

☒ Highlight all hit terms initially

24 and 21

EAST Search History Printout
6/27/06
BWR

 BRS form

IS&R form

 Image

Text

 HTML

U	1	Document ID	Issue Date	Pages	Title
1	<input type="checkbox"/>	US 6125052 A	20000926	43	Semiconductor memory device

Hits

 Details HTML

Ready

NUM